

Suitable for use with 12V ac or 12V dc applications, such as track lighting, automotive lighting and landscape lighting, the circuit delivers flat current regulation irrespective of input line and output LED voltage variation.

The design also features an autodetect circuit which, in combination with the NCP3065, allows input from a 12V dc or 12V ac supply while maintaining the targeted output current regulation.

The reference design has been optimised to drive up to eight high power LEDs in a range of applications. It uses a novel circuit configuration to achieve a power factor of in excess of 0.85 at 115V ac without the addition of a passive power factor correction network. This, says the company, reduces the component count and meets residential power factor requirements.

The design operates from a 90V ac to 265V ac universal input and is built around the ON Semiconductor NCP1014 switcher, which integrates a fixed frequency current mode controller with a 700V mosfet.

The reference design's basic control loop consists of a 235mV internal reference, a feedback comparator and two set dominant RS latches. Basically, says On Semi, the NCP3065 allows the power fet for the buck-boost stage to switch on as the feedback voltage falls below the reference voltage. The power fet will be then be forced off unconditionally during C_t ramp down. A resistor senses the inductor current and is fed to the NCP3065's feedback pin.

This application produces off time instantaneous (I_{valley}) inductor current control. A cycle of switch on time is only allowed to start once the off time inductor current crosses the V_{ref} threshold.

Since the controller does not provide integral PWM control and uses only a comparator trip point for feedback, the peak to average load current is not in direct proportion as it would be in a buck

converter. Therefore an input voltage feed forward compensation network is used to reduce the error due to the nonlinear response of the I_{out} vs V_{in} curve.

A resistive divider network consisting of three resistors is used to add V_{in} proportional voltage to the FB pin in order to reduce the load current as V_{in} is increased. This has the effect of flattening the curve and reducing the overall current error.

Since there is a half sine wave input to the buck-boost stage, there is a different operating point as compared with pure dc input. Since one of the goals for the reference design is small size, very little input capacitance is used past the full bridge rectifier.

As a result, the line voltage can drop as low as 3V, depending on the input capacitance selection. Therefore, the input to the converter is a full wave rectified sine wave.

Since the regulator is non functional at less than approximately 4V, there are dead spots in the regulation. So the end result is regulation for approximately 80% of the 120Hz line cycle and then no output for the remainder. This has the effect of reducing the average current by about 20% when operating with ac input.

Thermal consideration should be taken when running with supplies in excess of 12V ac and, in most applications, the module is potted to increase thermal dissipation. An additional ac compensation network is added to the V_{in} compensation to account for the different operating point.

The reference design comes with a full bill of materials, as well as a schematic and pcb layout information.

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